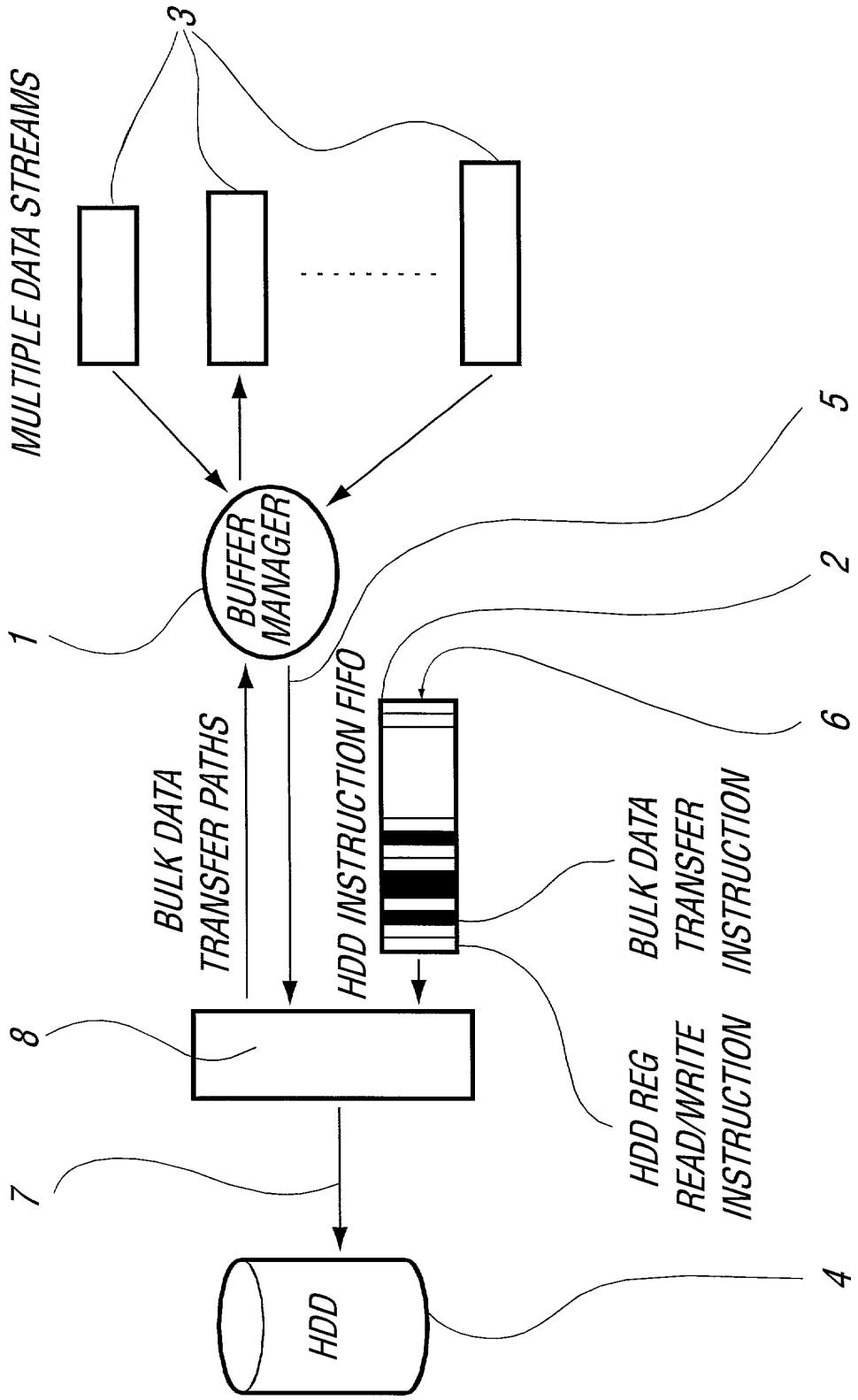

MULTIPLE DATA STREAMS
3
BUFFER MANAGER
1
5
2
6
BULK DATA TRANSFER PATHS
HDD INSTRUCTION FIFO
BULK DATA TRANSFER INSTRUCTION
8
HDD REG READ/WRITE INSTRUCTION
7
HDD
4

FIG. 1

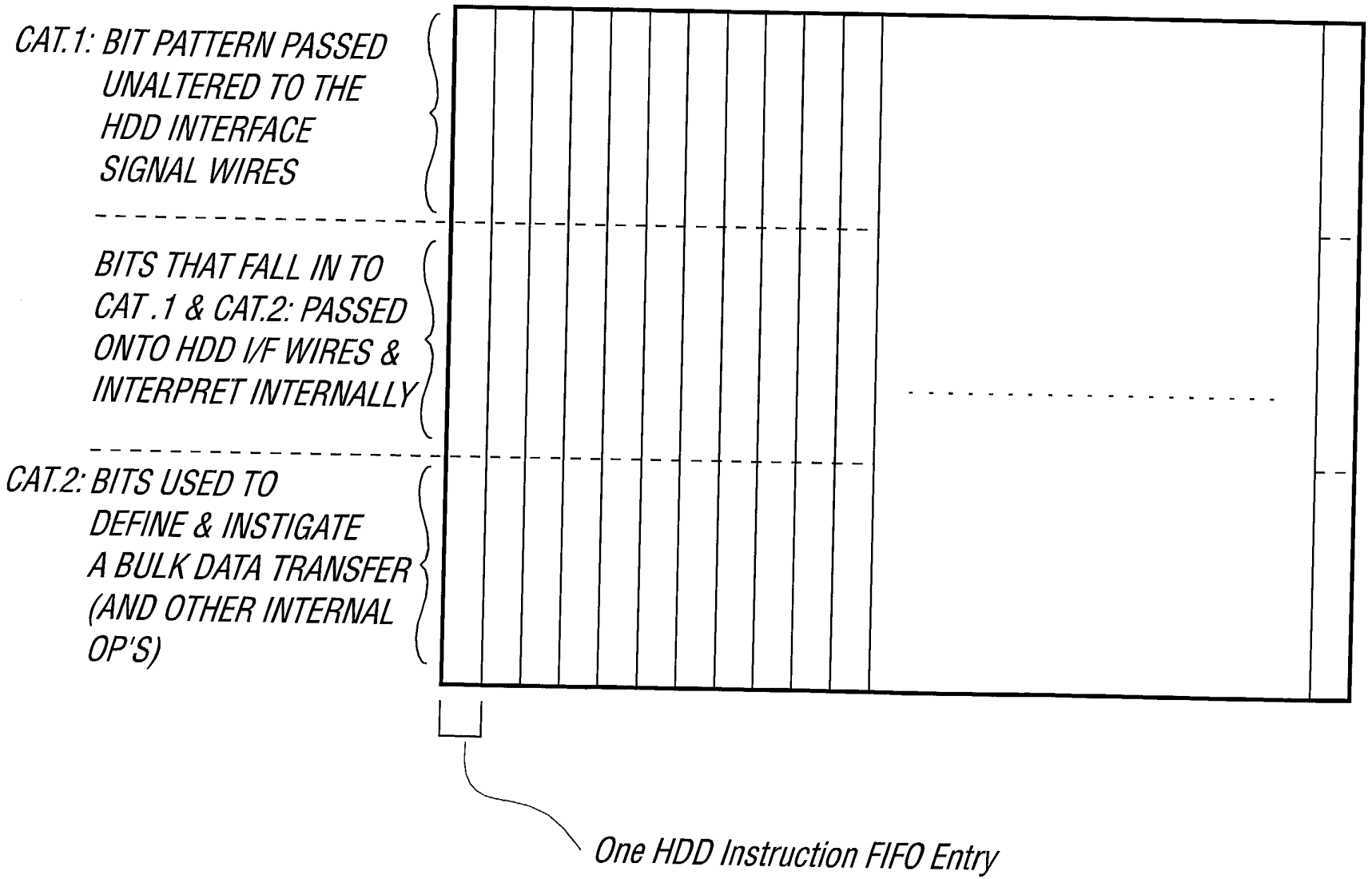

CAT.1: BIT PATTERN PASSED UNALTERED TO THE HDD INTERFACE SIGNAL WIRES
BITS THAT FALL IN TO CAT .1 & CAT.2: PASSED ONTO HDD I/F WIRES & INTERPRET INTERNALLY
CAT.2: BITS USED TO DEFINE & INSTIGATE A BULK DATA TRANSFER (AND OTHER INTERNAL OP'S)
One HDD Instruction FIFO Entry

FIG. 2